Amendments to the Abstract:

~~There are provided a synchronization circuit and a synchronization method which can obtain the synchronized signal stabilized in the frequency up to higher frequency band by satisfying both response characteristic and stability with a simplified structure. In the synchronization method, a first variable delay circuit generates a first pulse to be synchronized with a reference pulse, a second pulse which is leading in the phase to the first pulse and a third pulse which is delayed in the phase from the first pulse, the reference pulse and the first pulse are compared by a first phase comparing circuit, the reference pulse, second pulse and third pulse are compared by a second phase comparing circuit, a control voltage generating circuit forms a control voltage by giving priority to an comparison output of the second phase comparing circuit against a comparison output of the first phase comparing circuit, delay time of the first variable delay circuit is controlled, after the phases are matched, by forming the control voltage with the comparison output of the first phase comparing circuit.~~

Phase synchronization is achieved by forming a first pulse to be synchronized with a reference pulse, a second pulse leading in phase for a certain period relative to said first pulse and a third pulse delayed in phase for a certain period from said first pulse; comparing said reference pulse with said first pulse in a first comparing; comparing said reference pulse with said second pulse and said third pulse in a second comparing; and forming a control voltage by giving priority to a comparison output of said second comparing with respect to a comparison output of said first comparing, matching the phase of said reference pulse with the phrase of said second pulse or said third pulse, and matching, after said matching of phases, the

phrase of said reference pulse with the phase of said first pulse by forming said control, voltage from the comparison output of said first comparing.